Artificial neural networks and superconducting basic elements beyond von Neumann computer Superconducting neural networks for broadband signal receiving and processing

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State of the Art





Adiabatic superconducting logic demonstrates the highest energy efficiency

Orbital satellite communication system



Reception and processing of weak broadband signals



An adiabatic superconducting processor was created that consumes 15 fJ at clock speeds of 5 GHz

> Deep space exploration



Physical problem statement:investigation of the adiabatic evolution of systems based on the S-quantron for the construction of an energy-efficient element base of the SNN.

Subject: physical implementation of neurons and connections



IBM TrueNorth

Science 345, 668 (2014)

• Front Neurosci. 9, 141 (2015)



1M neurons 256M synapses, ~ 400 Mbit SRAM 46 x 10⁹ OPS (70 - 100 mW)

4096 cells (64 x 64) 100 Kbit SRAM for synapse memory (2 bits/synapse) 256 neurons ($f_c = 1$ KGz)

Memristors

CMOS + Memristors





	ITRI, IEDM 2008	NEC, VLSI 2010	Panasonic, IEDM 2008	Univ. + IMEC, IMW 2010	Fujitsu, IEDM 2007
Device	TiN/Ti/HfO _x /TiN	Ru/TiO _x /TaO _x /Ru	Pt/TaO _x /Pt	Au/NiO _x /TiN	Pt/Ti-doped
Reset	2V, 25uA	0.65V, 200uA	1.5V, 100uA	0.5V DC, 9.5uA	1.9V, 100uA
Set	2.3V	2.8V	2V	2.7V DC	2.8V
Form Voltage	3V	?	?	3.7V DC	3V
Switching Time	<10ns	<1us	<100ns	NA	10ns



$P \sim 10^{-4} W$

ANN implementation



M.L. Schneider et al., J. Appl. Phys. 124, 161102 (2018)

SFQ-based ANNs



Superconducting tensor processor unit



Estimations (SC TPU):

- Stream length = 256
- Cell area with = 0.3x0.3 ~ 0.1 μ^2
- ALU area = $2x256x0.1 \sim 50 \ \mu^2$
- Chip area = $1x1 \text{ cm}^2 = 10^8 \mu^2$
- 2x2x128x128 ~ 65x10³ ALUs/chip
- Available memory ~ $10^8/0.1 = 1$ Gbit/layer
- $f_{\rm c} = 40 \text{ GHz} (t_{\rm c} = 25 \text{ ps})$
- $E_{\text{bit}} = 2 \text{ aJ} (E_{\text{op}} = 2x256 = 512 \text{ aJ})$: $E_{\text{SCE}}/E_{\text{CMOS}} \sim 10^{-3}$
- 1chip = 2 active layers / 2 cores / 2 systolic arrays 128x128 ALUs
- 2x2x2x128x128x2x40x10⁹ = 10.5 POPS (~ 40 POPS four-chip module): Th_{sce}/Th_{смоs} ~ 10²
- 25 SC TPU ~ 1 EOPS
 - High clock frequency and 3D architecture allow
 2 orders of magnitude performance improvement
 - Low energy dissipation allows an order of magnitude

improvement in energy efficiency (including a cooling penalty)

Superconducting optoelectronic ANN



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neural block with synaptic weight update

J. M. Shainline et al., J. Appl. Phys. 124, 152130 (2018)

J. M. Shainline et al., J. Appl. Phys. 126, 044902 (2019)

Superconducting "hybrid" solutions

Fast and energy-efficient platform for quantum and neural network computing



Similar element bases!



Temperature levels

Sigma-neuron for perceptron



Sigma-neuron: main idea



Soloviev I. I., Schegolev A. E., Klenov N. V., Bakurskiy S. V., Kupriyanov M. Y., Tereshonok M. V., Golubov A. A. (2018). Adiabatic superconducting artificial neural network: Basic cells. *Journal of Applied Physics*, 124(15), 152113.

Sigma-neuron: optimization



Gauss-neuron for RBF-network



Gauss-neuron: main idea



Synapse: main idea





Learning cell: main idea











Magnetic rotary valve (thanks to A.A.G. and V.V.R. groups)



Qubit as a neuron?



N. V. Klenov, et al. *Low Temperature Physics*, 45(7):769–774, 2019. A. V. Bogatskaya, N. V. Klenov, et al. *Laser Physics Letters*, 16(5):056006, 2019.

THANK YOU FOR YOUR ATTENTION

Практические реализации: уменьшение размеров базовых элементов (I)

Научная Новизна. Созданы и апробированы методики для анализа процессов переноса заряда в компактных джозефсоновских элементах и фазовых батареях (с учетом особенностей влияния топологии при переходе к наноразмерным структурам), входящих как в состав ШП АЦП, так и в состав сигнального процессора, нейросетевого и квантового блока обработки сигнала



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Golikova T.E., Hubler F., Beckmann D., Klenov N.V., Bakurskiy S.V., Kupriyanov M.Yu., Batov I.E., Ryazanov V.V. Critical current in planar SNS Josephson junctions // JETP Letters. — 2013. — Vol. 96, no. 10. — P. 668–673.

Сверхпроводниковый ReLU (Rectifier Linear Unit) сверточных ИНС



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Современные подходы к созданию элементной базы нейронных сетей



КМОП нейрон

ICC

W1 - Mc

WO - Mca

Состояние исследований. Джозефсоновские структуры





Состояние исследований.

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• Общая идея работы:

разрабатываем принципы создания «проблемных» элементов перспективной когнитивной широкополосной системы приема и обработки сигнала



